Name of the	module/subject		ESCRIPTION FORM	ode	
Circuits	,		-	1010324311010320173	
Field of study	y		Profile of study	Year /Semester	
Electrical Engineering			(general academic, practical) (brak)	1/1	
Electrical Engineering Elective path/specialty			Subject offered in:	Course (compulsory, elective	
Lioouvo pauli	ropoolary	-	Polish	obligatory	
Cycle of stud	dy:		Form of study (full-time,part-time)	·	
	First-cyc	le studies	part-ti	part-time	
No. of hours				No. of credits	
Lecture:	20 Classes	s: 20 Laboratory: -	Project/seminars:	5	
Status of the	course in the study	program (Basic, major, other)	(university-wide, from another field	,	
		(brak)	(b	rak)	
Education are	eas and fields of sci	ECTS distribution (number and %)			
technica	l sciences			5 100%	
т	Fechnical scie	ences		5 100%	
Respons	sible for subje	ect / lecturer:			
-	-				
	eszek Kasprzyk				
	eszek.Kasprzyk@	put.poznan.pl			
tel. 6166					
		aarina			
	of Electrical Engin owo 3A 60-965 Pc				
ul. Piotro	owo 3A 60-965 Po		d social competencies:		
ul. Piotro Prerequi	owo 3A 60-965 Po	s of knowledge, skills an	d social competencies:		
ul. Piotro Prerequi	owo 3A 60-965 Pc	s of knowledge, skills and Basic information form math and	h physics at level of High School.		
ul. Piotro Prerequi 1 Kr	owo 3A 60-965 Pc	s of knowledge, skills and Basic information form math and	I physics at level of High School.	tive self-education in field of	
ul. Piotro Prerequi 1 Kr 2 Sk 3 Sc	owo 3A 60-965 Pc isites in term nowledge kills ocial	s of knowledge, skills and Basic information form math and Skills in understanding and inter science related with chosen aca Student should have consciousr	I physics at level of High School. pretation of information and effect demic discipline.		
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Lecture:

- assess the knowledge and skills listed on the written and oral exam of the theory of circuits.

Auditorium exercises:

- assess skills of solving accounting exercises in range of analysis of electric and electronic circuits ? verification skills on every exercises and two tests during the semester.

Obtaining additional points activity during exercises, in particular way for:

- proposing to discuss additional aspects of the subject,
- effective use of knowledge obtained during solving of given problem,
- comments related to improve teaching material,
- aesthetics of solved problems and reports ? within homework.

Course description

Electric signals and classification, basic definitions in field of circuits with lumped parameters and circuits with distributed parameters, elements of electric circuits, arrow convention for voltage and current, electric circuits laws, methods of analysis of direct current circuits and one- and three-phases alternating current circuits (Kirchhoff?s laws, Mesh-Current Method, Node-Voltage Method), circuits theorems: (Norton?s theorem, Thevenin?s theorem, Tellegen?s theorem), real power, reactive power an complex power, energy in electric circuits, maximum power transfer theorem, magnetic coupled circuits, resonance effect, measurements of power and energy in electric circuits. Solving accounting tasks in field of analysis of direct current circuits, one- and three-phase alternating current circuits.

Basic bibliography:

1. Kurdziel R.: "Podstawy elektrotechniki", WNT, Warszawa 1973.

2. Bolkowski S.: "Teoria obwodów elektrycznych", WNT, Warszawa 1998.

- 3. Szabatin J., Śliwa E.: "Zbiór zadań z teorii obwodów. Część 1", Wydawnictwo Politechniki Warszawskiej, Warszawa 1997.
- 4. Mikołajuk K., Trzaska Z.: "Zbiór zadań z elektrotechniki teoretycznej", WNT, Warszawa 1978.

5. Kurdziel R.: "Podstawy elektrotechniki", WNT, Warszawa 1973.

6. Bolkowski S.: "Teoria obwodów elektrycznych", WNT, Warszawa 1998.

7. Szabatin J., Śliwa E.: "Zbiór zadań z teorii obwodów. Część 1", Wydawnictwo Politechniki Warszawskiej, Warszawa 1997.

8. Mikołajuk K., Trzaska Z.: "Zbiór zadań z elektrotechniki teoretycznej", WNT, Warszawa 1978.

Additional bibliography:

1. Krakowski M.: "Elektrotechnika teoretyczna", PWN, Warszawa 1978.

2. Chua L. O., Desoer C. A., Kuh E. S.: "Linear and nonlinear circuits", McGraw-Hill Inc., New York 1987.

3. Jastrzębska G., Nawrowski R.: "Zbiór zadań z podstaw elektrotechniki", Wydawnictwo Politechniki Poznańskiej, Poznań 2000.

4. Krakowski M.: "Elektrotechnika teoretyczna", PWN, Warszawa 1978.

5. Chua L. O., Desoer C. A., Kuh E. S.: "Linear and nonlinear circuits", McGraw-Hill Inc., New York 1987.

6. Jastrzębska G., Nawrowski R.: "Zbiór zadań z podstaw elektrotechniki", Wydawnictwo Politechniki Poznańskiej, Poznań 2000.

Result of average student's workload

	Time (working hours)					
	20					
	20					
	10					
	10					
	10					
	20					
	30					
	20					
	5					
Student's workload						
hours	ECTS					

Practical activities	0	0